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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/711,959

Conf. No.: 5958

Filing Date: 10/15/2004

Art Unit: 2825

Applicant: Heng et al.

Examiner: Lam, Nelson C.

Title: INTEGRATED CIRCUIT
SELECTIVE SCALINGDocket No.: BUR920040201US1
(IBMB-0059)

COMMISSIONER FOR PATENTS

DESTINATION FACSIMILE NUMBER: 571-273-8300

Transmitted herewith is:

Amendment in 13 pages

in the above identified application.

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS

In response to the Office Action of January 25, 2006, please amend the above-referenced patent application as follows:

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